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Substitute for Form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary.)				Complete if Known	
				Application Number	09/540,611
				Filing Date	March 31, 2000
				First Named Inventor	Carl M. Ellison
				Group Art Unit	2782
				Examiner Name	Unassigned
Sheet	1	of	2	Attorney Docket Number	042390.P8112

U.S. PATENT DOCUMENTS

Exam. Initials.	Document Number	Date	Name	Class	Sub-class	Filing Date (if appropriate)
ECT	4,278,837	07/14/81	Best			
	4,521,852	06/04/85	Guttag			
	5,022,077	06/04/91	Bealkowski et al.			
	5,293,424	03/08/94	Holtey et al.			
	5,421,006	05/30/95	Jablon et al.			
	5,473,692	12/05/95	Davis			
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	B1					

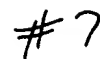
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Substitute for Form 1449A/PTO (Modified)

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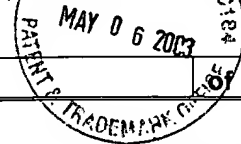
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U.S. PATENT DOCUMENTS

Exam. Initial*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (If known)			
ECT		6,505,279		Phillips et al.	01-07-2003	
		5,079,737		Hackbarth	01-07-1992	
		5,628,022		Ueno et al.	05-06-1997	
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		6,085,296		Karkhanis et al.	07-04-2000	
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		Office ³	Number ⁴	Kind Code ⁵ (If known)				
ECT		-	WO 97/29567		Fortress U & T Ltd.	08-14-1997		
		-	WO 01/75565		Intel Corporation	10-11-2001		
		-	WO 01/75595		Intel Corporation	10-11-2001		
		-	WO 01/27723		Hewlett-Packard Company	04-19-2001		
		-	WO 01/27821		Hewlett-Packard Company	04-19-2001		

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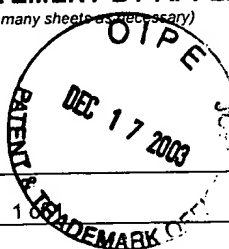
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Complete if Known

Application Number	09/540611
Filing Date	March 31, 2000
First Named Inventor	Ellison, Carl M.
Group Art Unit	2782
Examiner Name	Unassigned

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Attorney Docket No: 42P08112

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US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
ECT /	DE-DE4217444	12/03/1992	Toyohisa, Imada, et al.			
ECT /	EP-EP0473913	03/11/1992	Farrell, Joel A.			abs

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
ECT	/	BRANDS, STEFAN, "Restrictive Blinding of Secret-Key Certificates", SPRINGER-VERLAG XP002201306, (1995), Chapter 3	
	/	DAVIDA, GEORGE I., et al., "Defending Systems Against Viruses through Cryptographic Authentication", <u>Proceedings of the Symposium on Security and Privacy</u> , IEEE Comp. Soc. Press, ISBN 0-8186-1939-2, (May 1989)	
	/	KASHIWAGI, KAZUHIKO, et al., "Design and Implementation of Dynamically Reconstructing System Software", <u>Software Engineering Conference</u> , Proceedings 1996 Asia-Pacific Seoul, South Korea 4-7 Dec. 1996, Los Alamitos, CA USA, IEEE Comput. Soc, US, ISBN 0-8186-7638-8, (1996)	
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	/	SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture", <u>Department of Computer Science and Engineering, University of California, San Diego, La Jolla, CA, (Nov. 2001)</u>	

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Substitute Disclosure Statement Form (PTO-1449)

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Group Art Unit	2782
Examiner Name	
Attorney Docket Number	042390.P8112

Sheet	1	of	2
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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

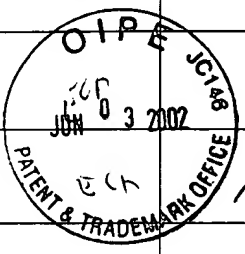
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OTHER ART – NO PATENT LITERATURE DOCUMENTS					
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		"M68040 USER'S MANUAL", 1993, MOTOROLA INC., page 1-5 – page 1-9, page 1-13- page 1-20, page 2-1-page 2-3, page 4-1, page 8-9-page 8-11			
		"INTEL 386 DX MICROPROCESSOR 32-BIT CHMOS MICROPROCESSOR WITH INTEGRATED MEMORY MANAGEMENT", 12/31/95, INTEL INC., page 32-56; figure 4-14			
		JOE HEINRICH:"MIPS R4000 MICROPROCESSOR USER'S MANUAL", 1994, MIPS TECHNOLOGY INC., MOUNTAIN VIEW, CA, pages 67-79			
		BERG C: "HOW DO I CREATE A SIGNED APPLLET?", DR. DOBB'S JOURNAL, M&T PUBL., REDWOOD CITY, CA, US, VOL. 22, NO. 8, 8 '97, page 109-111, 122			
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Sheet 1 of 1

Attorney Docket No: 42P08112

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Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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/	WO-WO0175564	10/11/2002	Herbert, Howard C., et al.			
/	WO-WO02086684	10/31/2002	Proudlar, Graeme J.			
/	WO-WO0217555	02/28/2002	Ford, Warwick			

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	/	CHIEN, ANDREW A., et al., "Safe and Protected Execution for the Morph/AMRM Reconfigurable Processor", 7th Annual IEEE Symposium, FCCM '99 Proceedings, XP010359180, ISBN 0-7695-0375-6, Los Alamitos, CA, (4/21/1999),209-221	
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ECT	/	GOLDBERG, R., "Survey of virtual machine research," IEEE Computer Magazine 7(6), pp. 34-45, 1974.	
↓	/	GUM, P.H., "System/370 Extended Architecture: Facilities for Virtual Machines," IBM J. Research Development, Vol 27, Number 6, pp. 530-544, November 1983	
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↓	/	LAWTON, K., "Running Multiple Operating Systems Concurrently on an IA32 PC Using Virtualization Techniques," http://www.plex86.org/research/paper.txt ; 11/29/1999; pp. 1-31.	

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